

**On Page 1, after the Description please insert the following:**

**CROSS-REFERENCE TO RELATED APPLICATIONS**

This application claims priority based on International Patent Application No.  
PCT/FR2004/050173, entitled "Method for Production of a Semiconductor Device with Auto-  
Aligned Metallisations" by Pierre-Jean Ribeyron and Marc Pirot. This application also claims  
the priority of the French application filed on April 29, 2003 under number 03 50136, which is  
incorporated by reference.